# UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 7,151,865 B2

Page 1 of 2

APPLICATION NO.: 10/735903

DATED

: December 19, 2006

INVENTOR(S)

: Toshihiko Ouchi

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

## COLUMN 3:

Line 19, "cross sectional" should read --cross-sectional--.

Line 52, "cross sectional" should read --cross-sectional--.

#### COLUMN 4:

Line 9, "a optical" should read -- an optical--.

Line 18, "a optical" should read -- an optical--.

#### COLUMN 6:

Line 37, "an transmission" should read --a transmission--.

### COLUMN 7:

Line 32, "overlie" should read --overlay--.

Line 51, "through via" should read --via--.

Line 62, "cross sectional" should read --cross-sectional--.

#### **COLUMN 8:**

Line 4, "though a via" should read --via--.

#### COLUMN 10:

Line 19, "This" should read -- The--.

Line 49, "Po;" should read --Po,--.

### COLUMN 14:

Line 2, "a" should read --an--.

Line 50, "chip size" should read --chip-size--.

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: December 19, 2006

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN 16:

Line 20, "chip" should read --chips--.

Signed and Sealed this

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Seventeenth Day of June, 2008

JON W. DUDAS Director of the United States Patent and Trademark Office